

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Osamu SUZUKI, et al.  
Serial No: 10/722,597  
Filed: November 28, 2003  
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND  
SEMICONDUCTOR INTEGRATED CIRCUIT CHIP THEROF  
Group: 2894  
Examiner: Monica LEWIS

**PETITION FOR AN EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

June 15, 2009

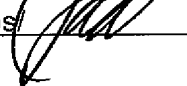
Sir:

In the matter of the above-identified application, Applicants hereby respectfully petition for an extension of time to permit filing of a response within the third month subsequent to expiration of the shortened statutory period set in the Final Rejection of December 15, 2008.

A credit card payment form in the amount of \$1,110.00 to cover the fee required for the requested extension of time is attached hereto.

It is respectfully requested that any shortage in the fee be charged to the Deposit Account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (520.43306X00).

Respectfully submitted,

/Larry N. Anagnos   
Larry N. Anagnos  
Registration No. 32,392  
ANTONELLI, TERRY, STOUT & KRAUS, LLP  
(703) 312-6600

Attachment  
LNA:dlh